

FIG. 1A

100

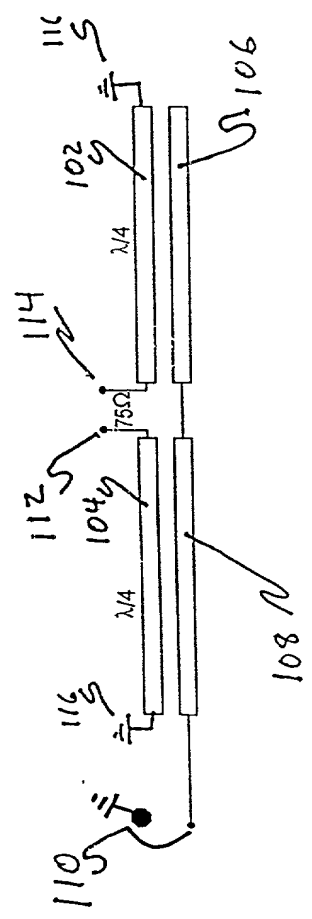


Figure 1B

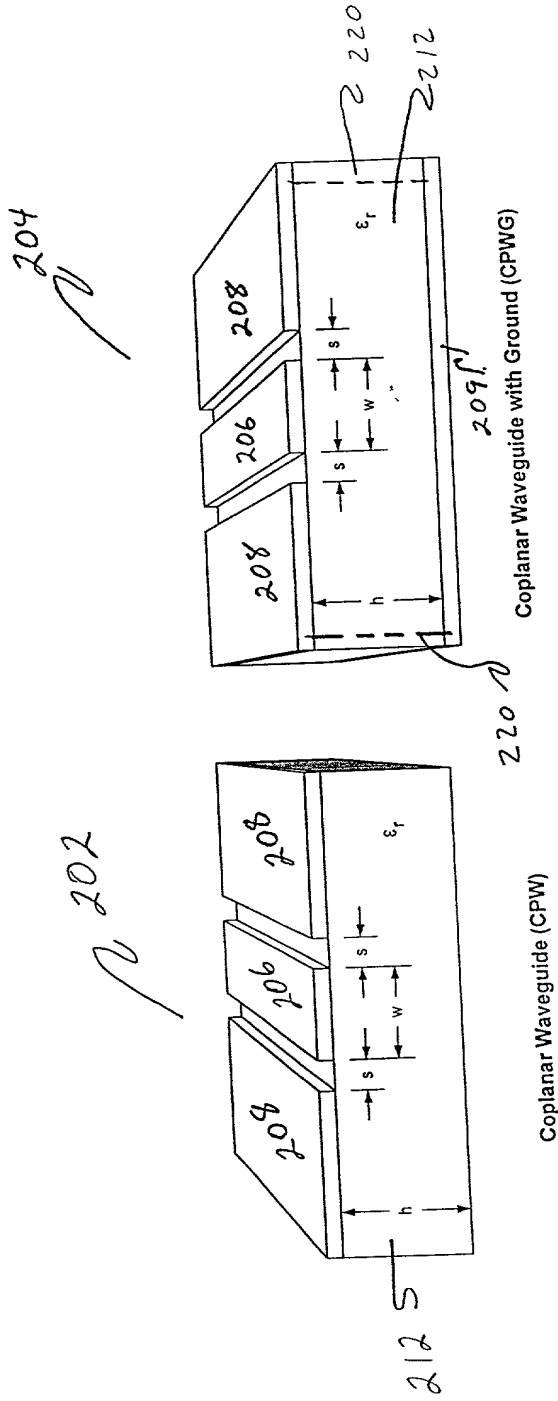


FIG 2A

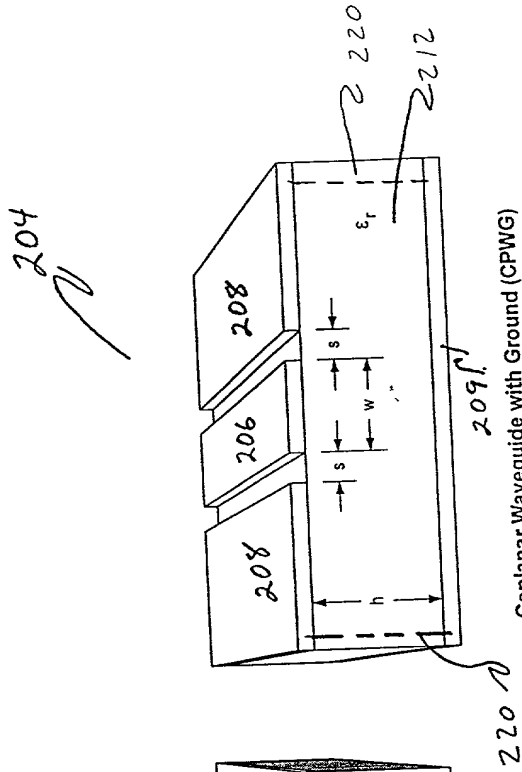


FIG 2B

300

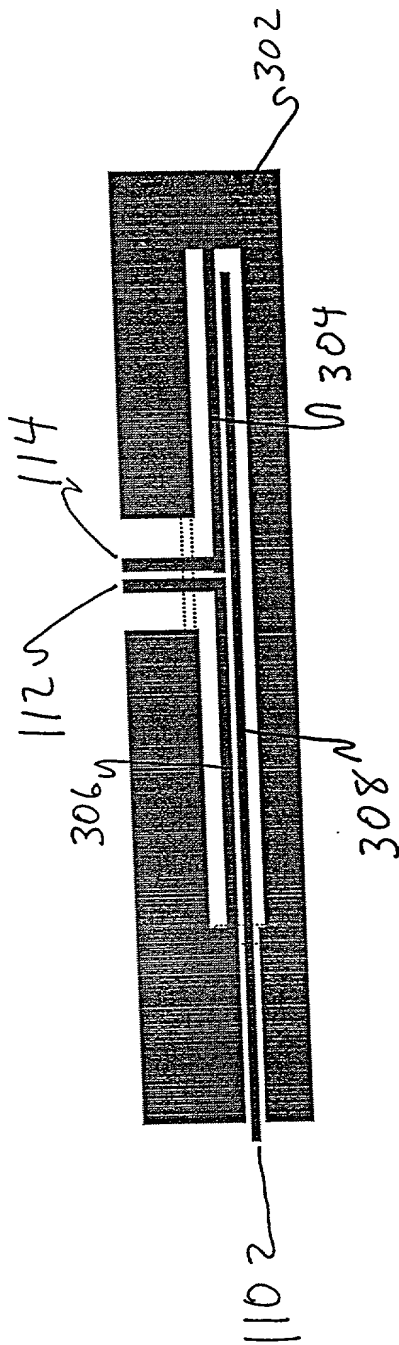


Figure 3

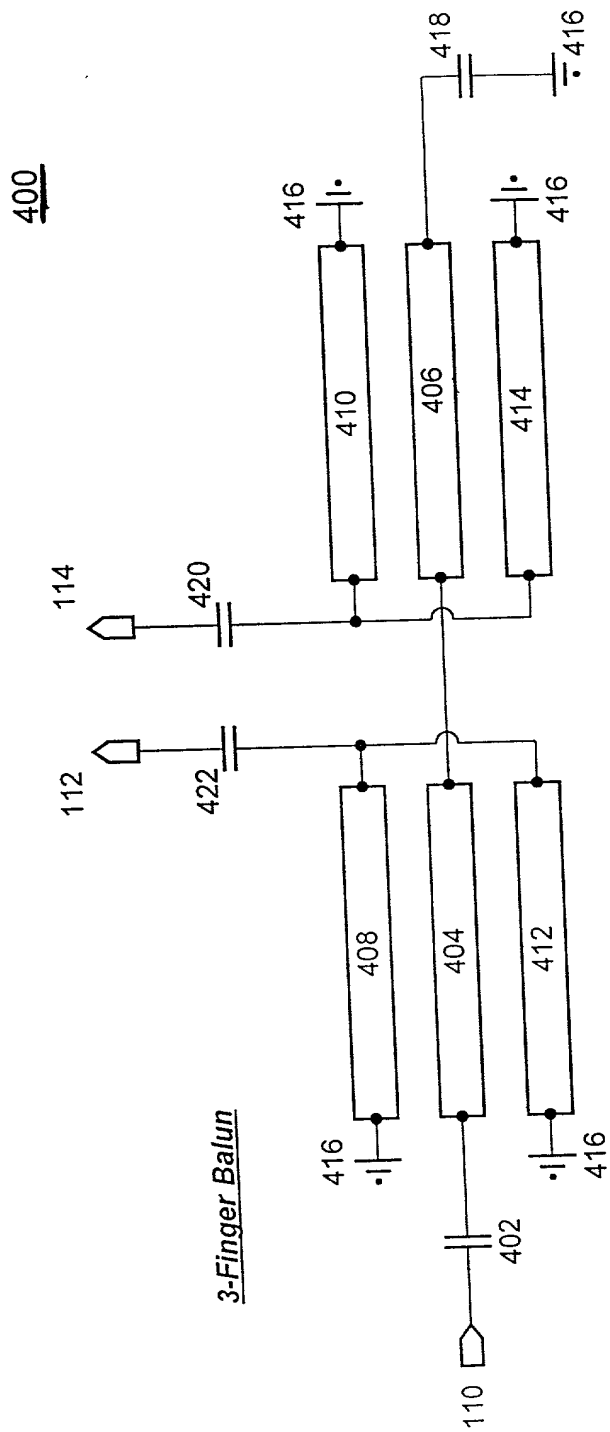


FIG. 4A

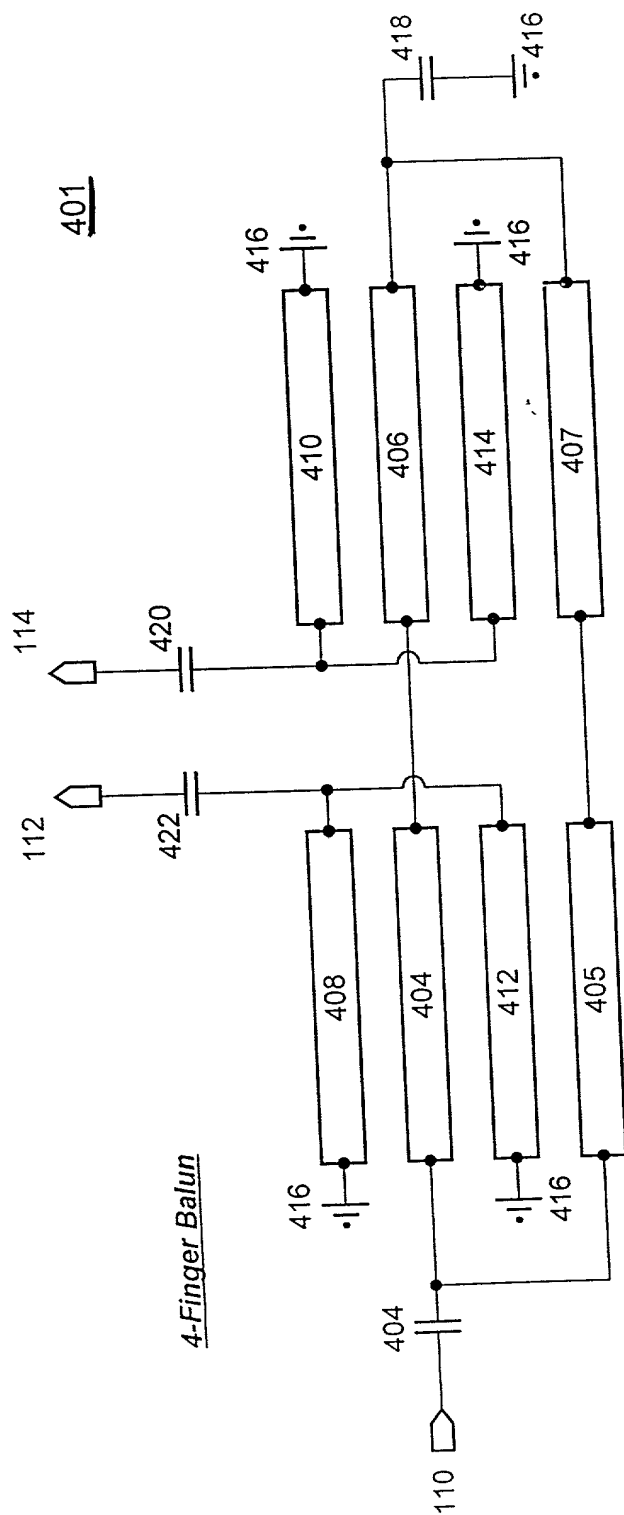


FIG. 4B

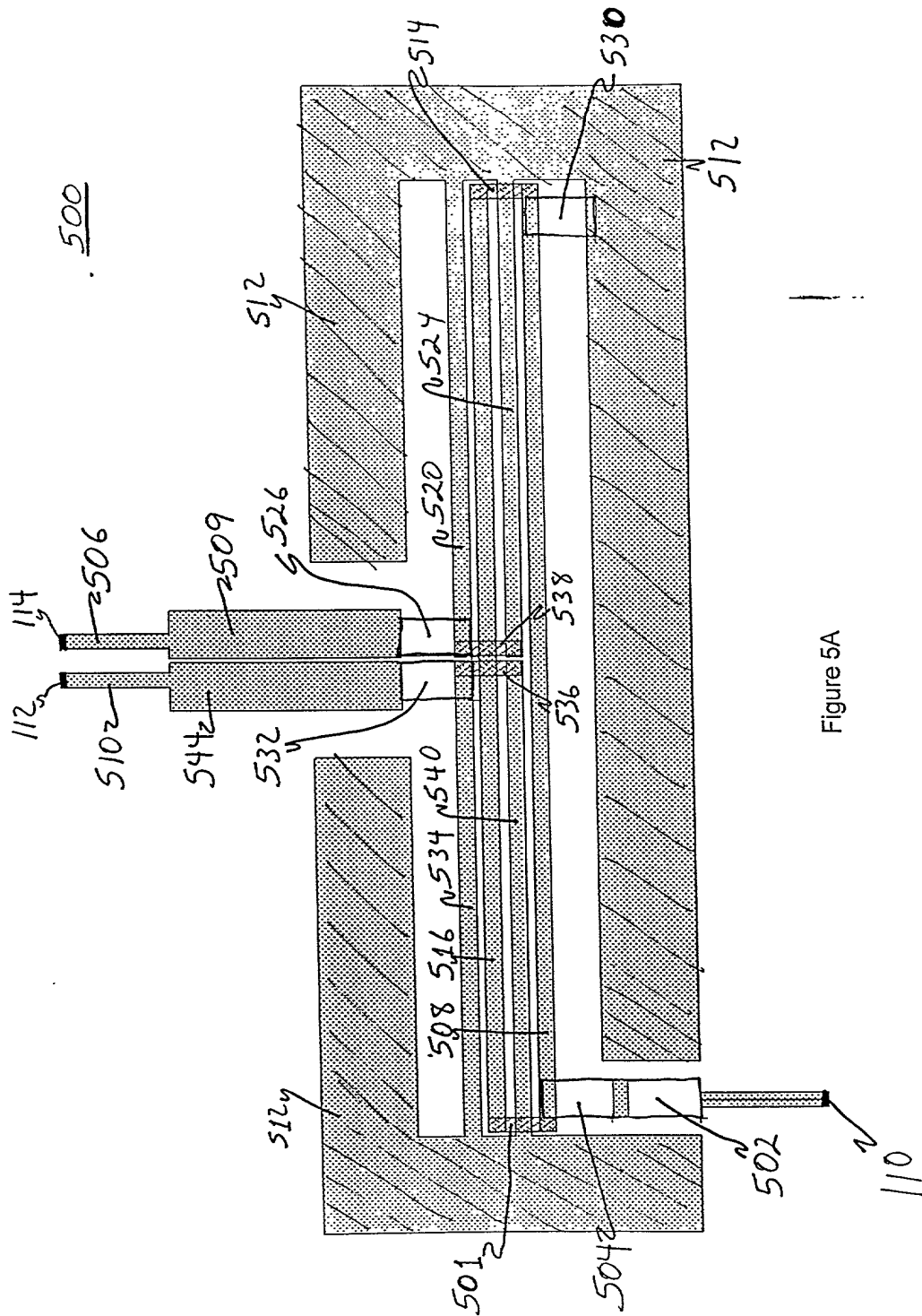


Figure 5A

550

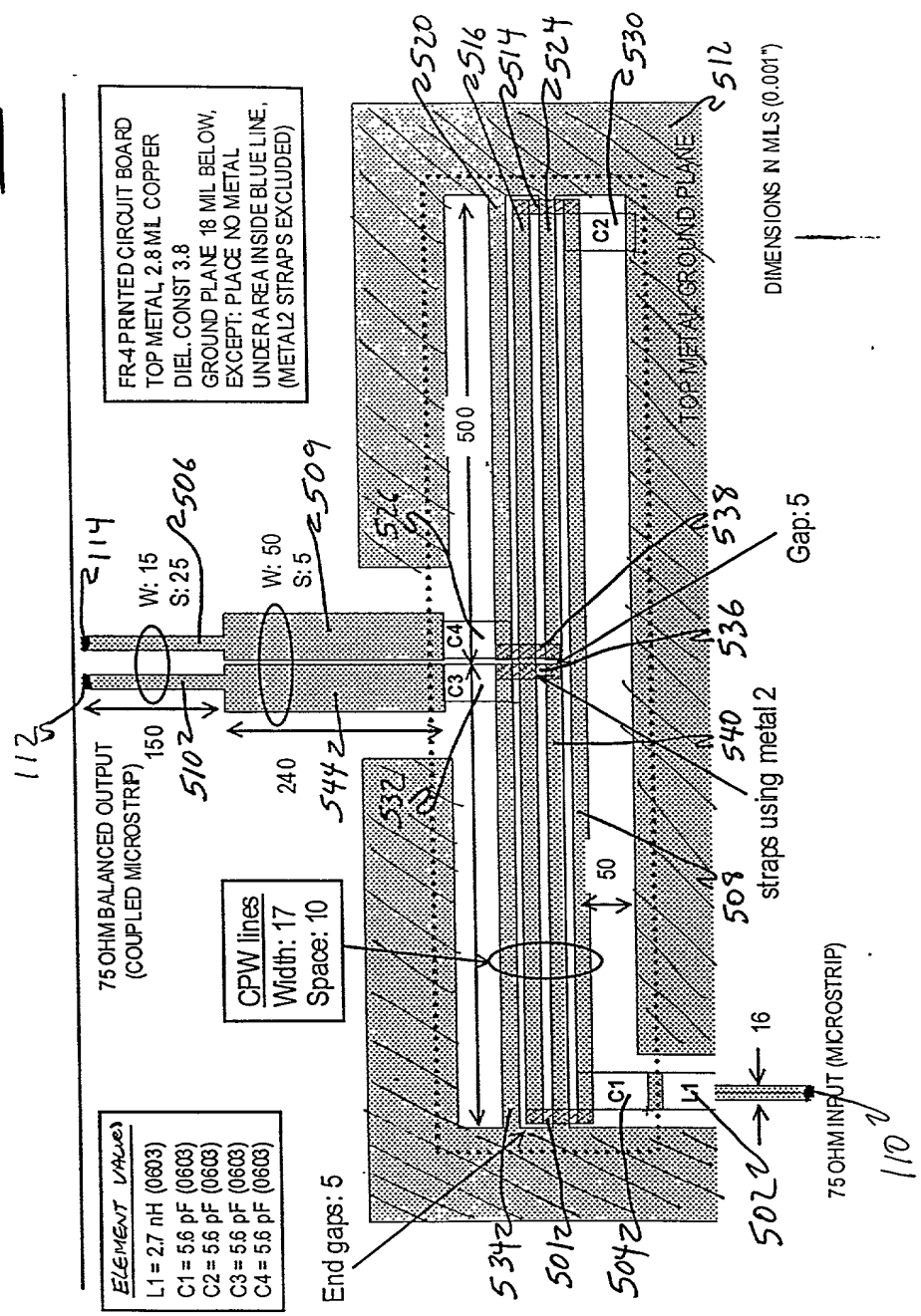


FIG 5B

2 Layer Printed Circuit Board

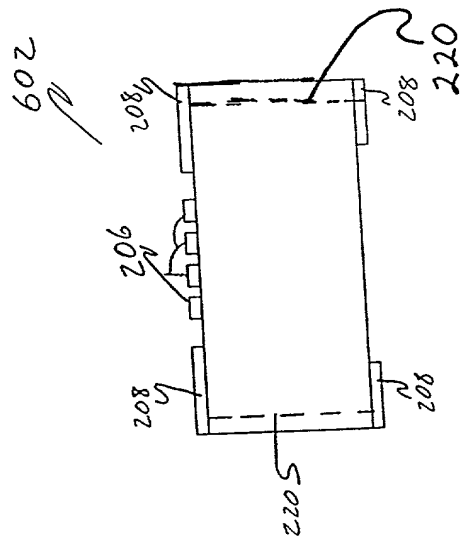


FIG 6A

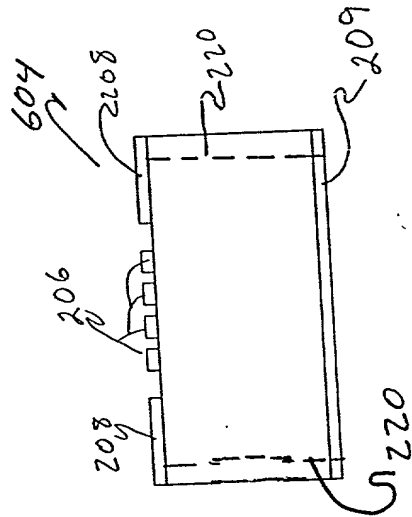


FIG 6B

4 Layer Printed Circuit Board

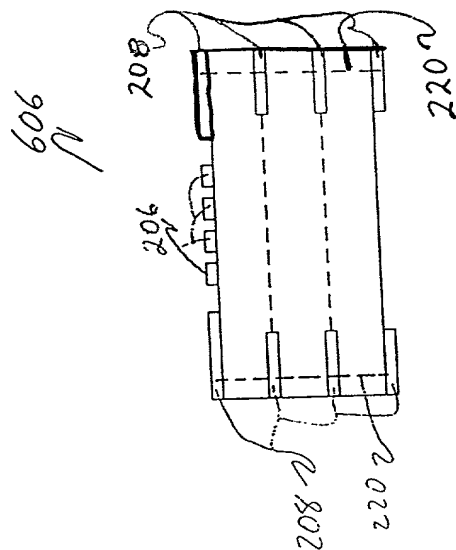


FIG 6C

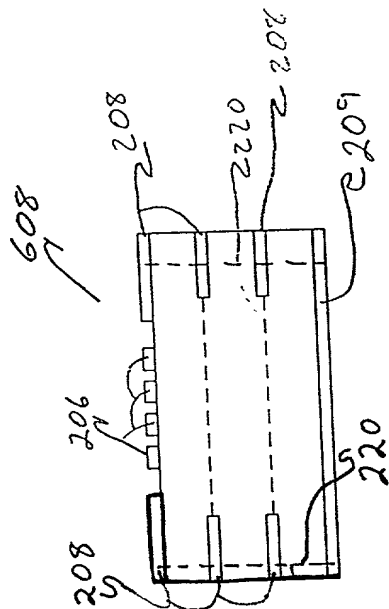


FIG 6D

6 Layer Printed Circuit Board

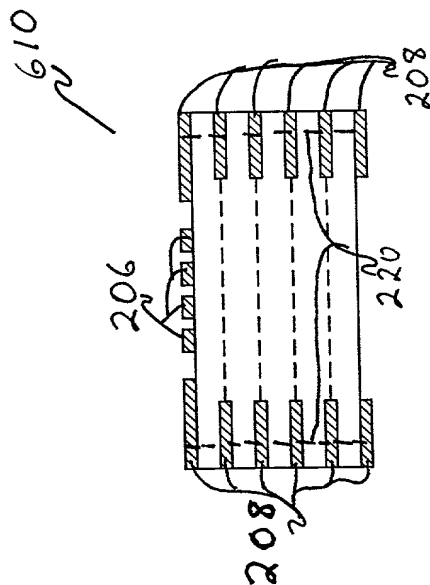


FIG. 6E

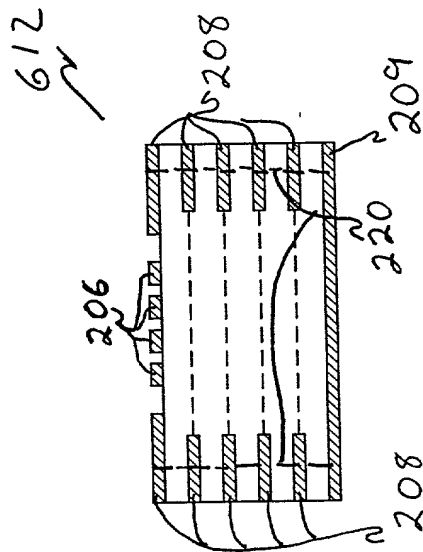


FIG. 6F

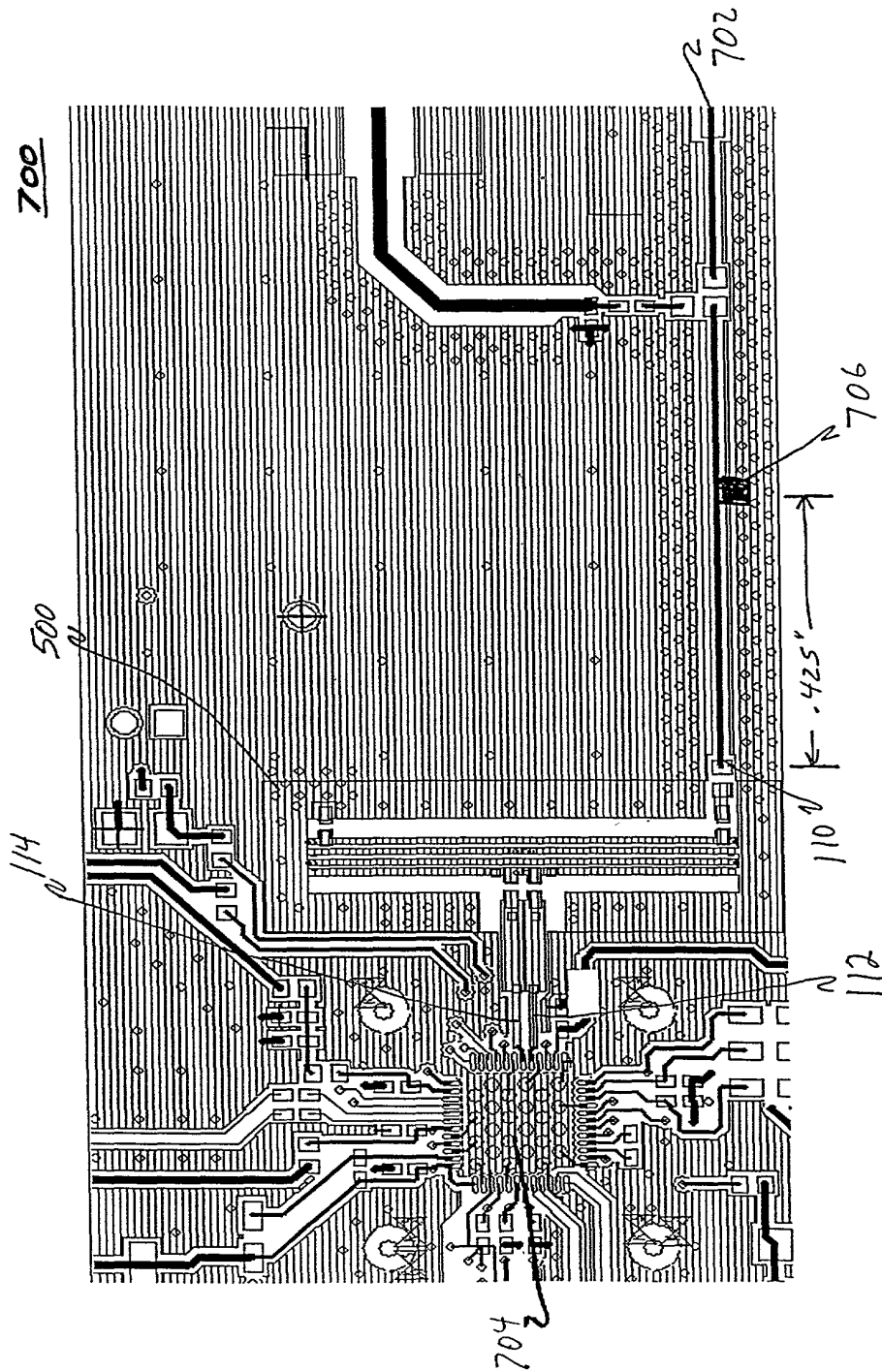


Figure 7

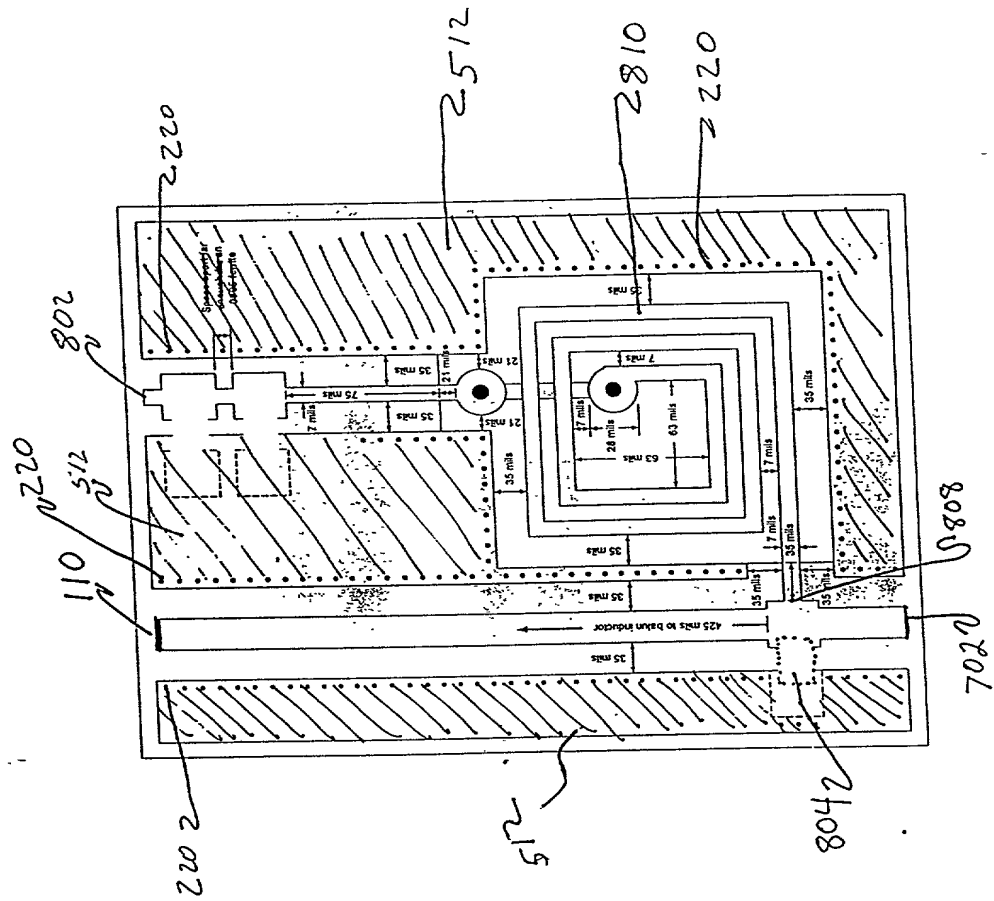
$$\begin{array}{r} 800 \\ \hline \end{array}$$


Figure 8

900

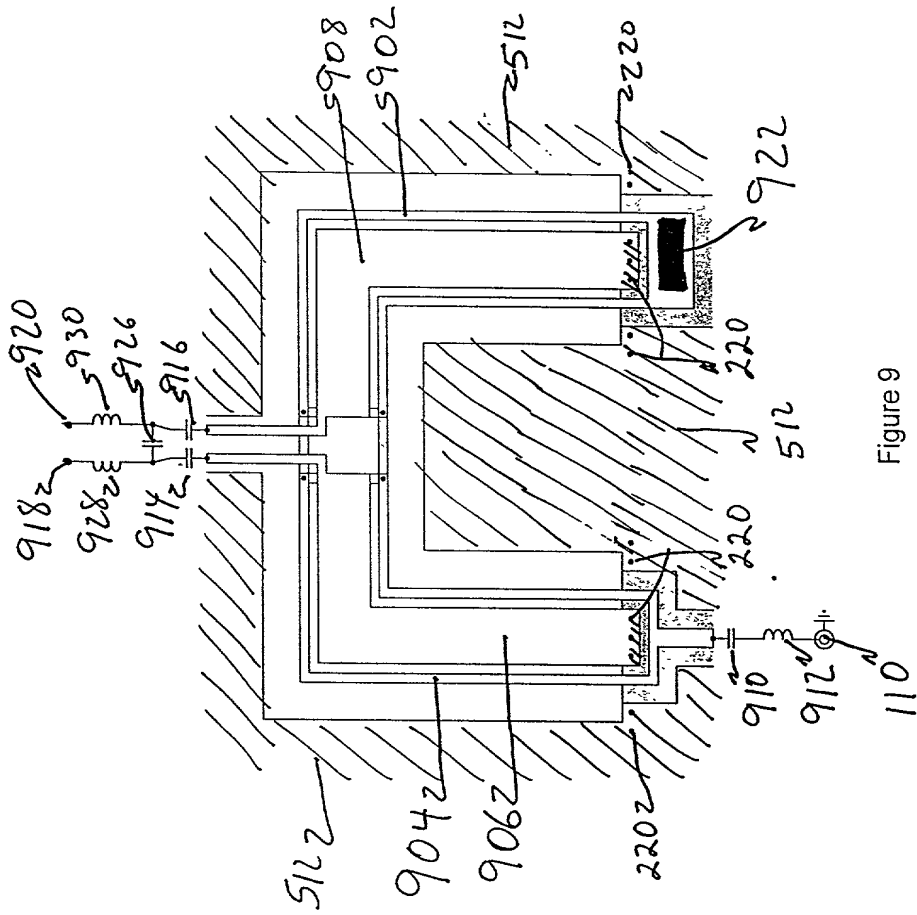


Figure 9

FIG 10

512 v

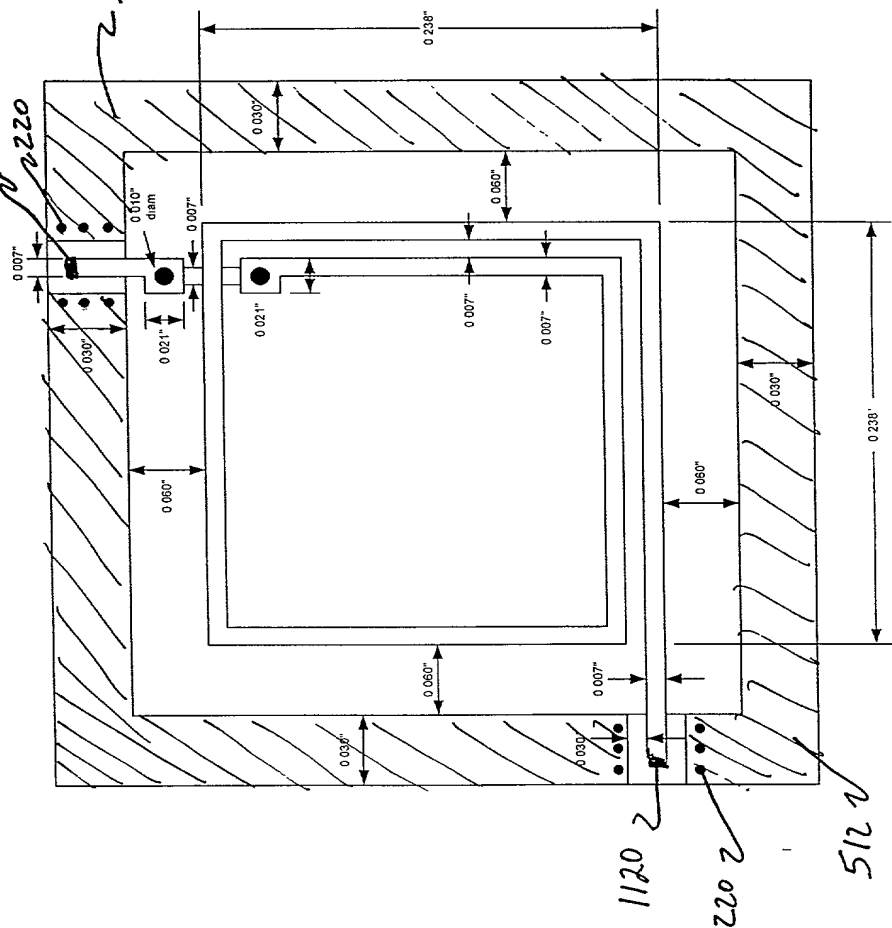


FIG 11

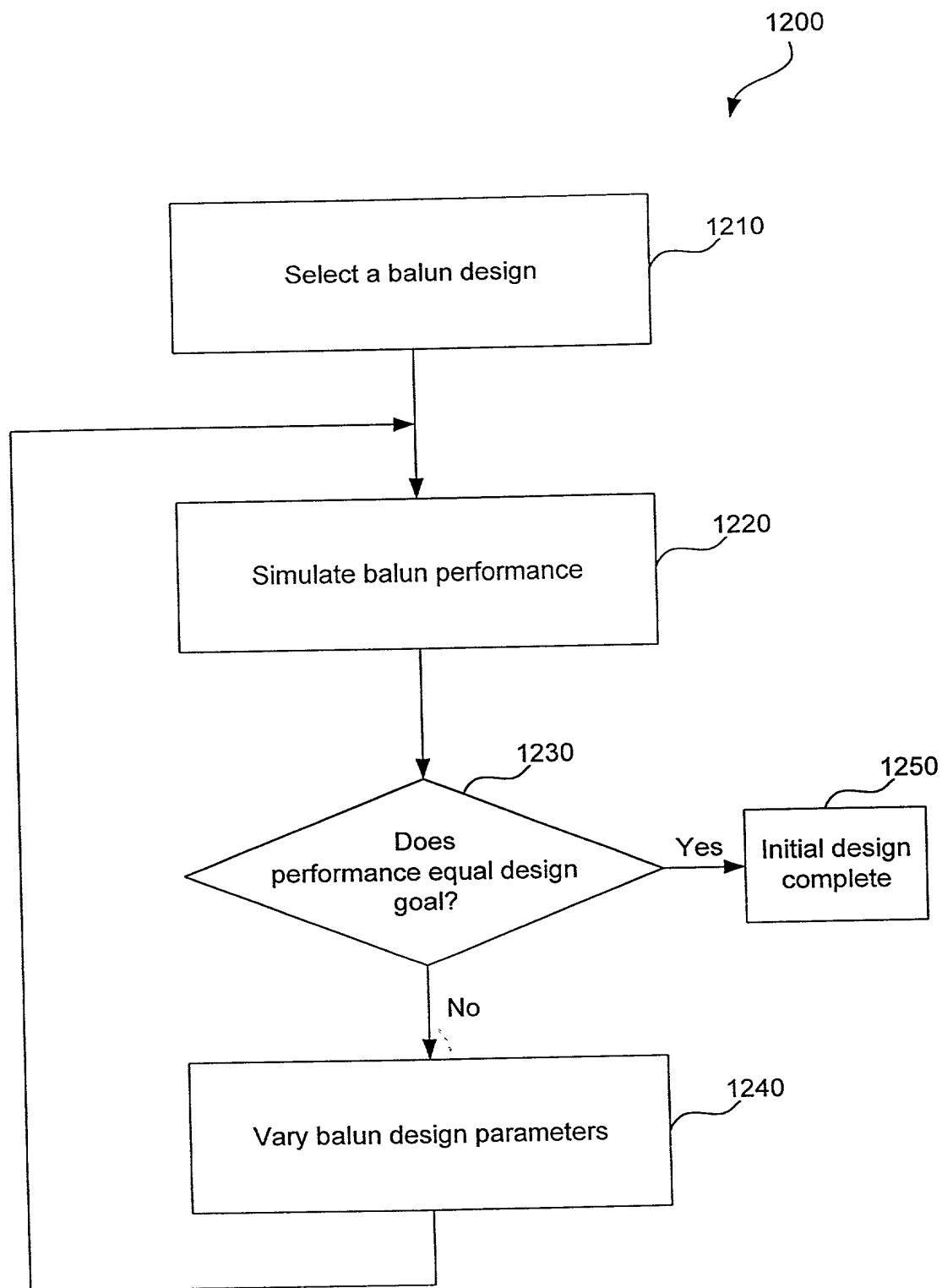


FIG. 12

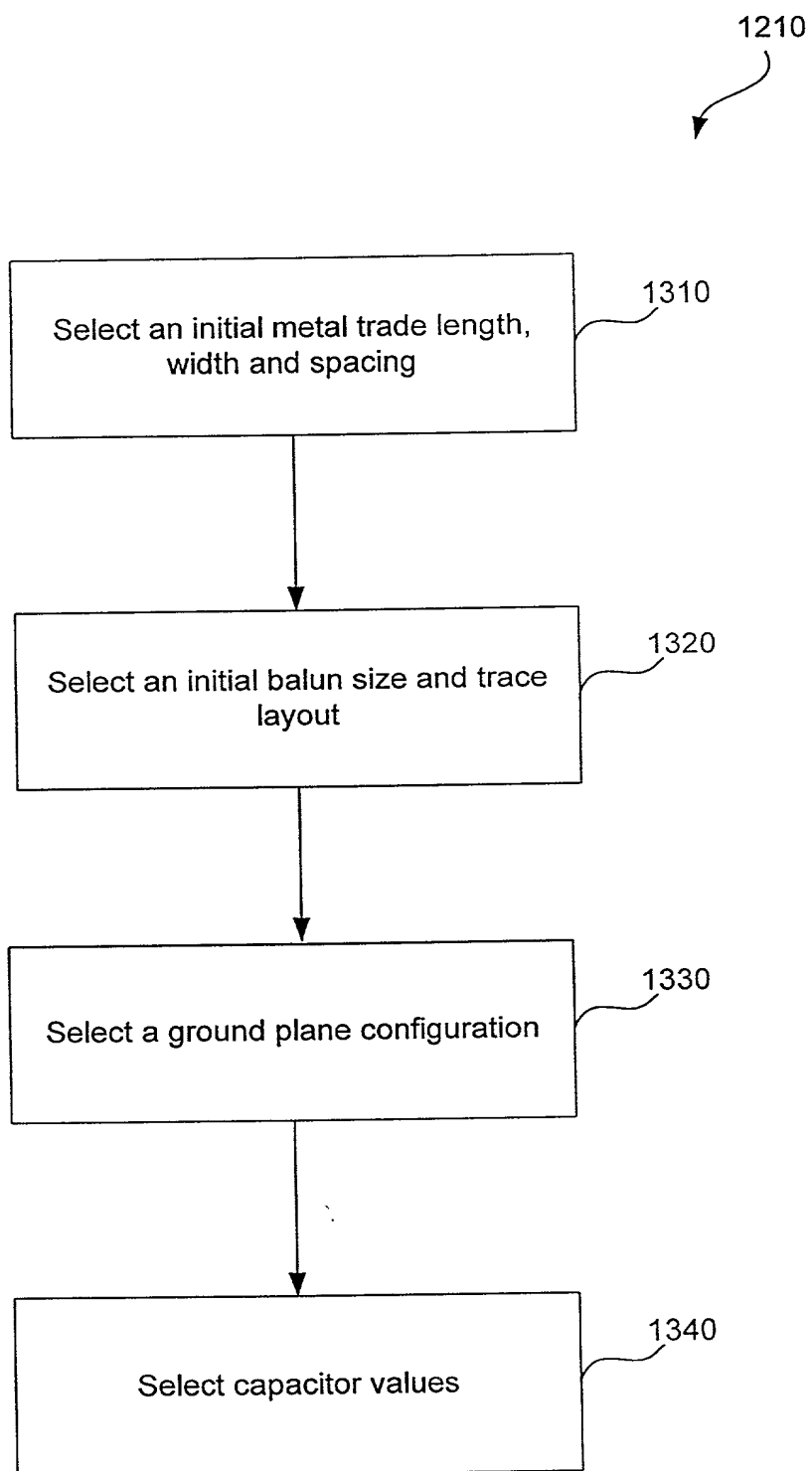


FIG. 13

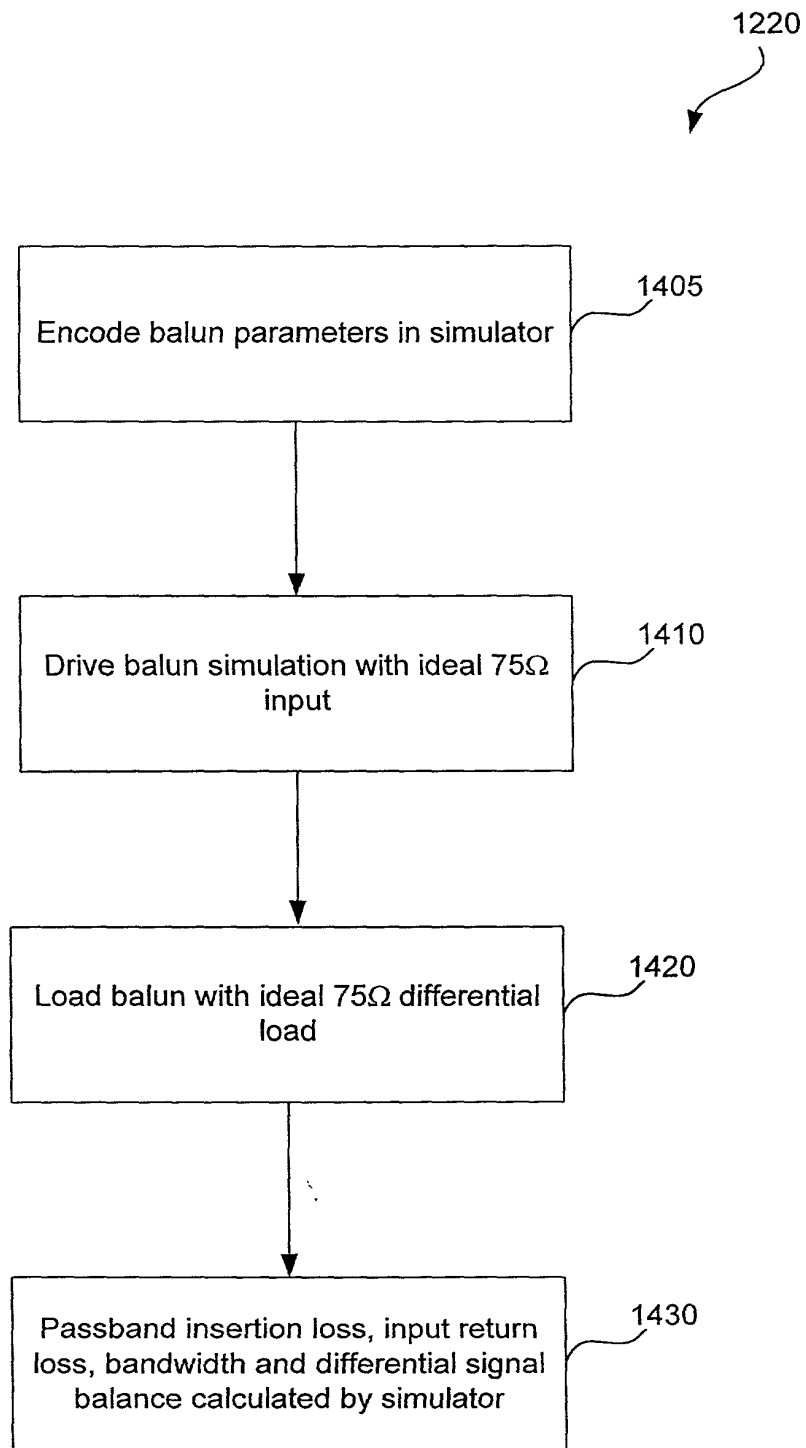


FIG. 14

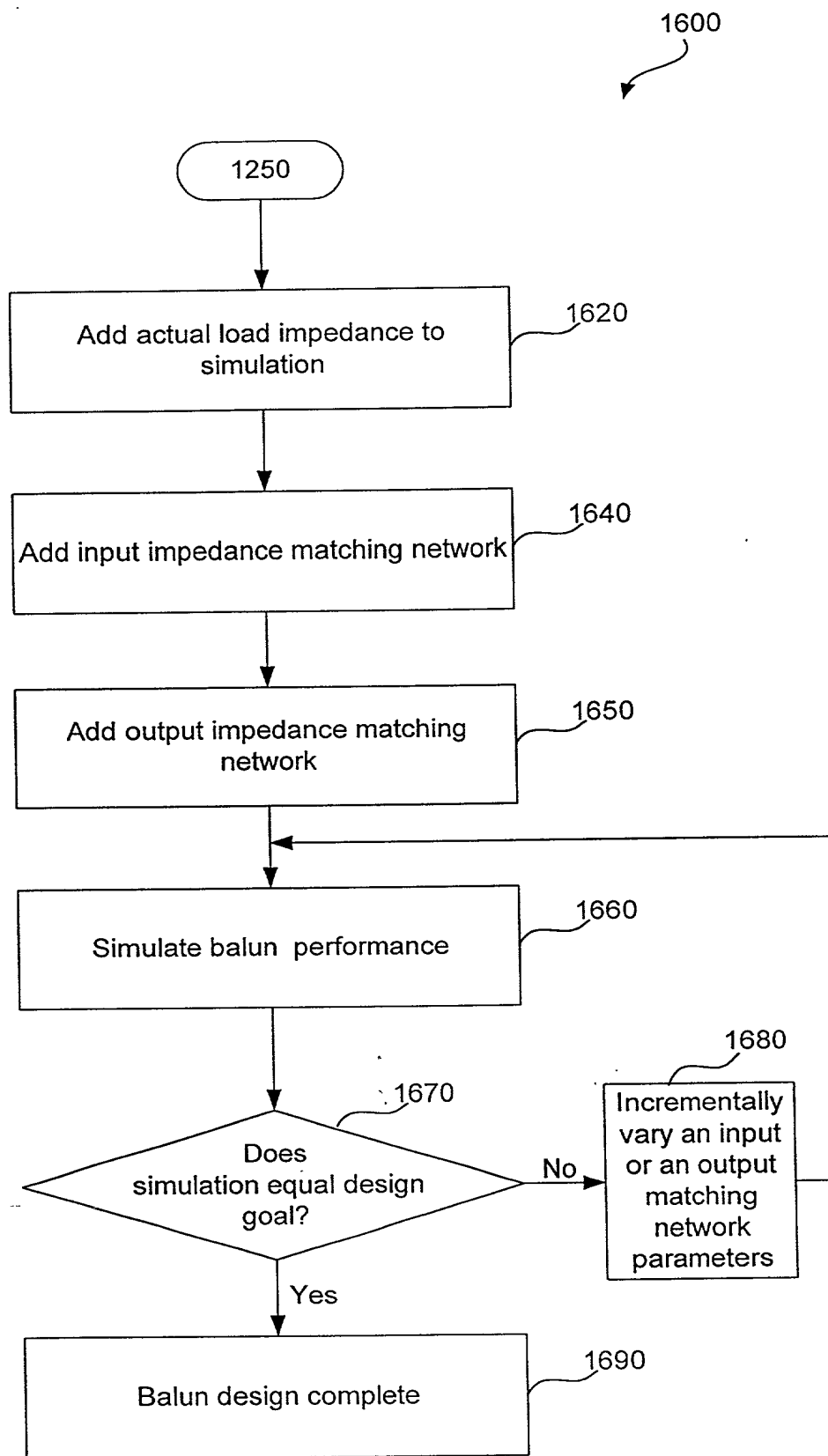


FIG. 16